



X-953 US
09/981,503

PATENT
Conf. No.: 6920

IN THE UNITED STATES PATENT OFFICE

Applicants: L. James Hwang et al.
Assignee: Xilinx, Inc.
Title: "A Method and System for Modeling and Automatically Generating an Electronic Design from a System-Level Environment"
Serial No.: 09/981,503 Filing Date: 10/16/2001
Examiner: Thai Q. Phan Art Unit: 2128
Docket No.: X-953 US Conf. No.: 6920

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated March 13, 2006, reconsideration and allowance of all claims are respectfully requested in view of the remarks that follow.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks being on page 8 of this paper.